

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Reducing Coupling Effect on Reference Voltages When Output Buffers Implemented with Low Voltage Transistors Generate High Voltage Output Signals

Application Number :

Confirmation Number:

First Named Applicant: Rajat CHAUHAN

Attorney Docket Number: TI-38154

Art Unit:

Examiner:

Search string: (4704547).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4704547	1987-11-03	Kirsch			

Signature

Examiner Name	Date